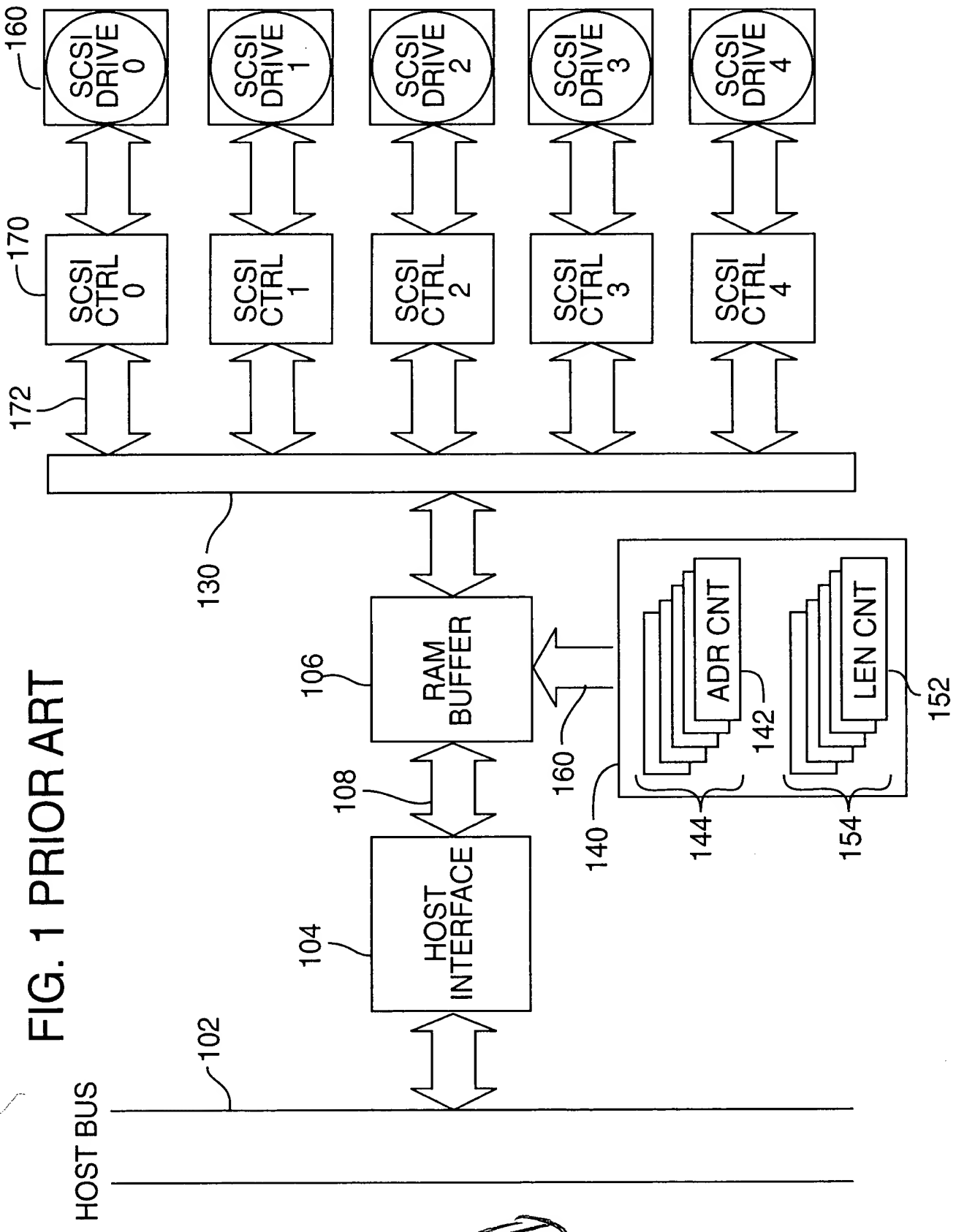


FIG. 1 PRIOR ART

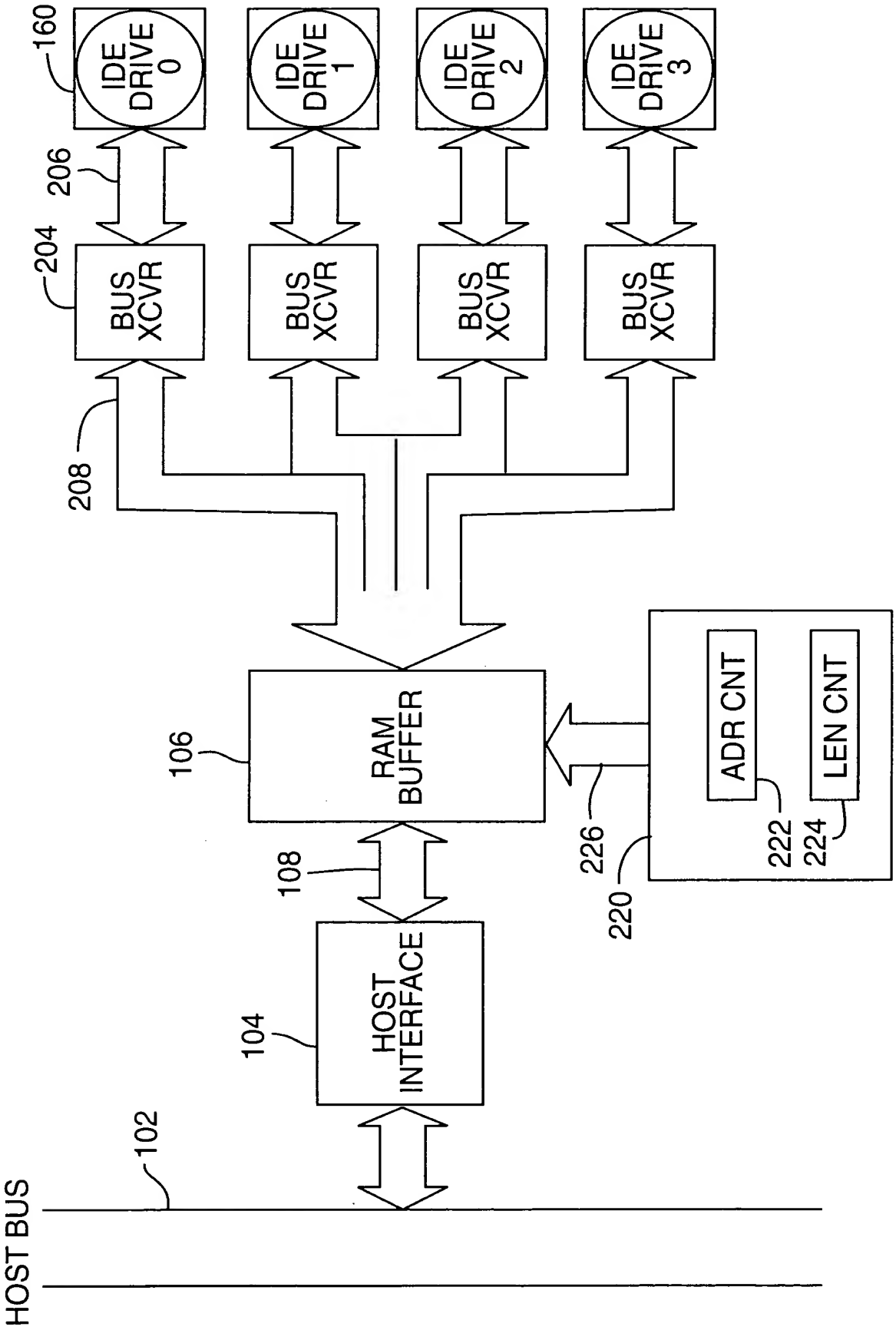


FIG. 2

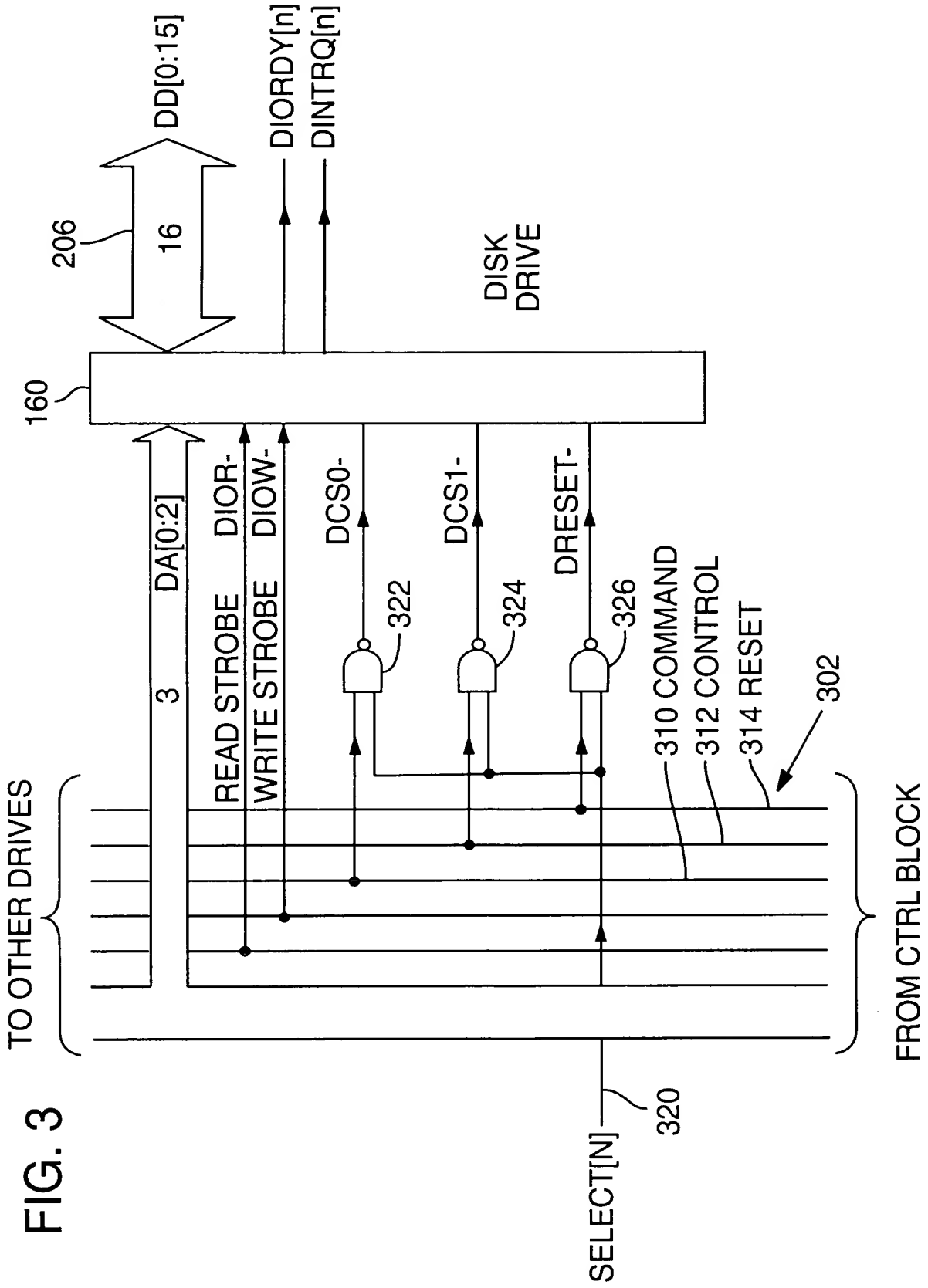


FIG. 3

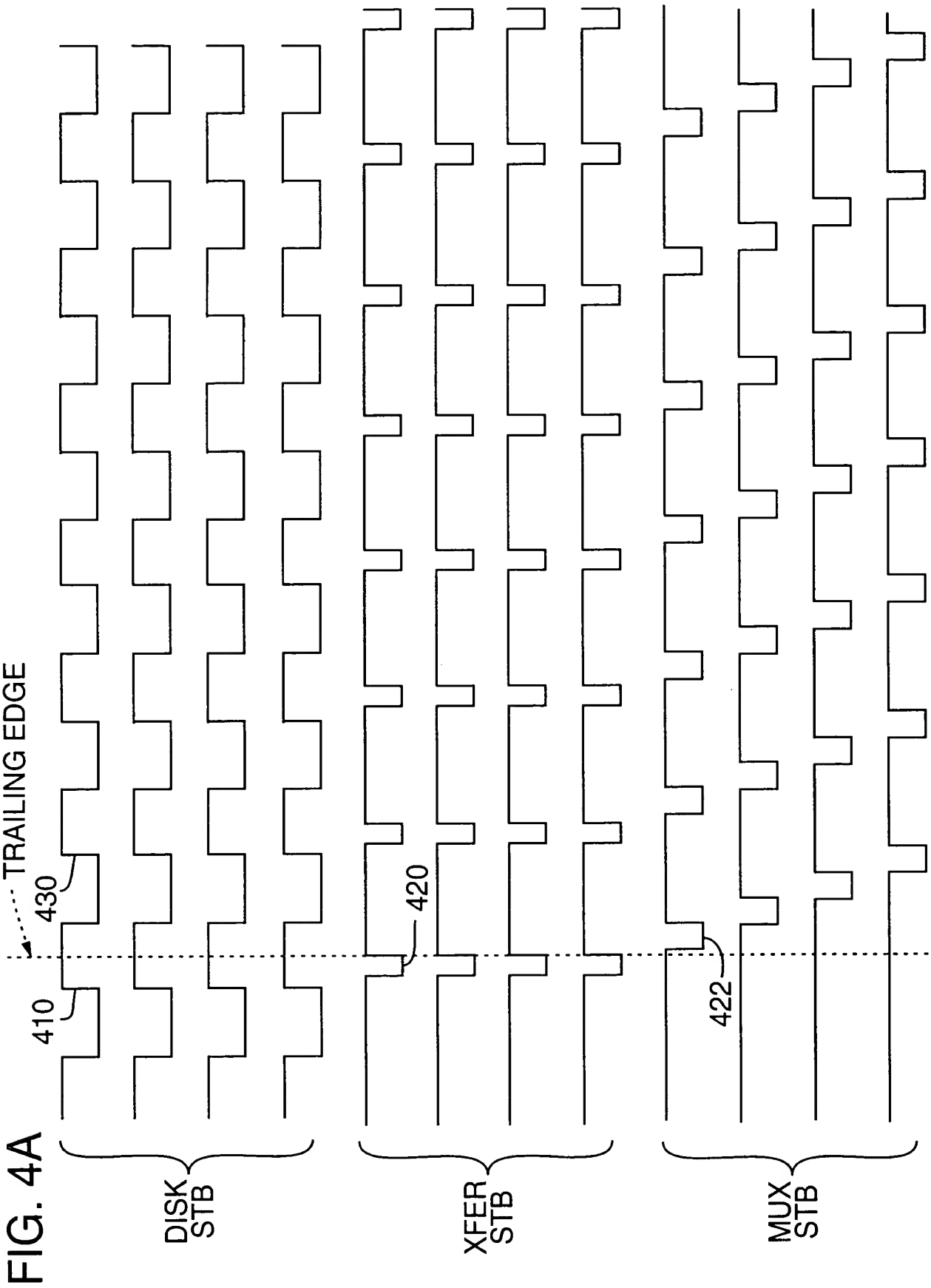


FIG. 4A

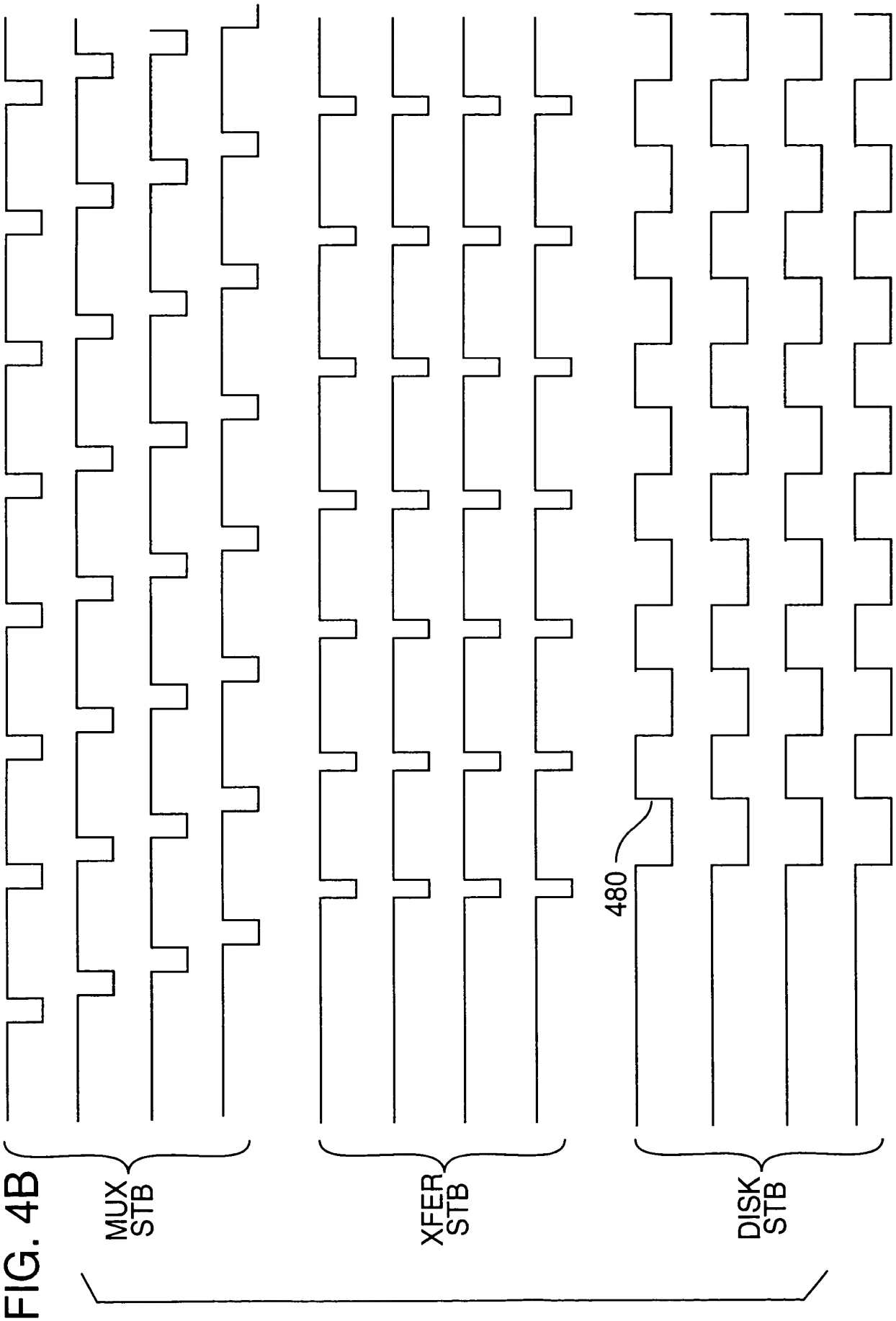


FIG. 4B

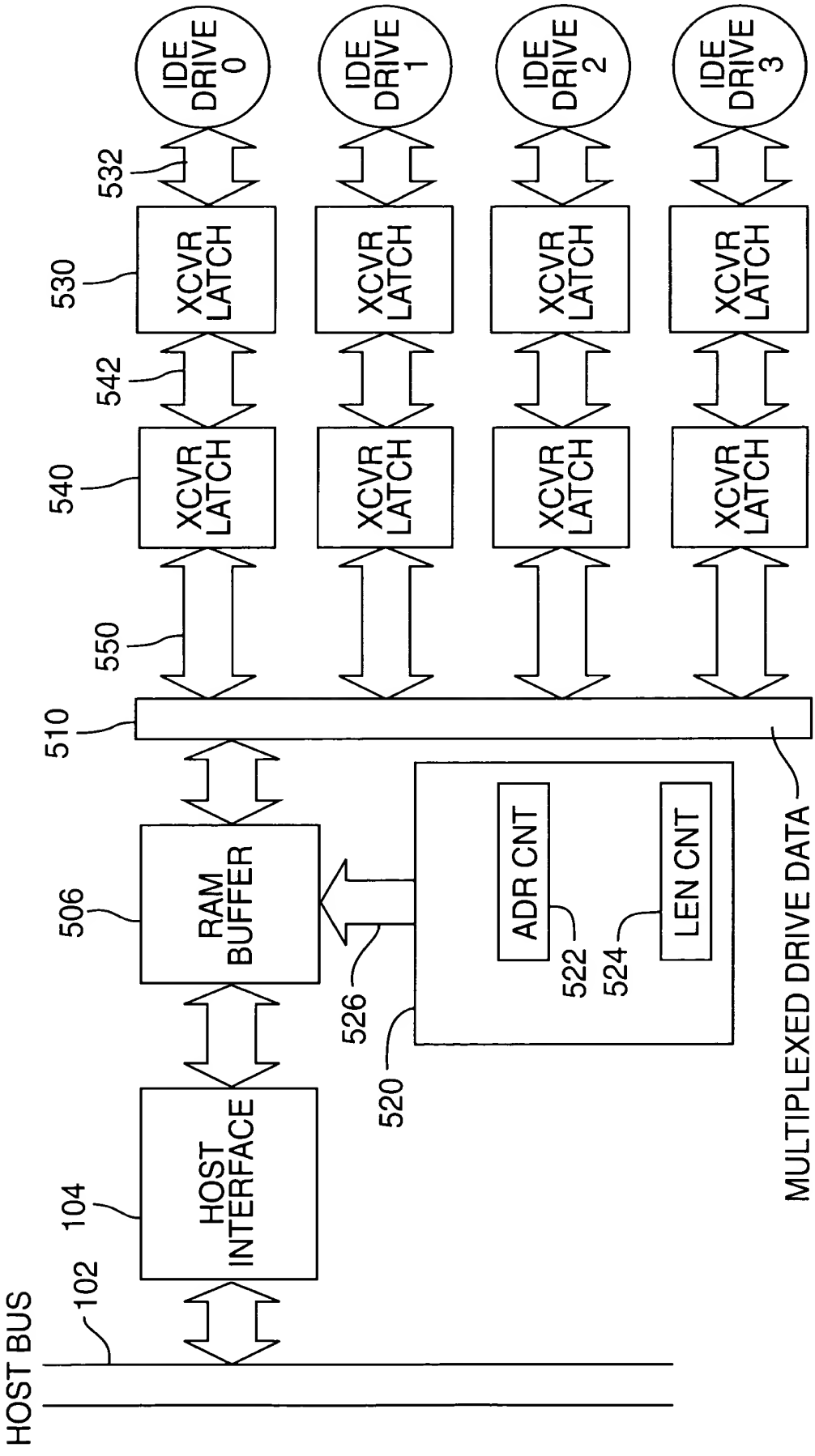
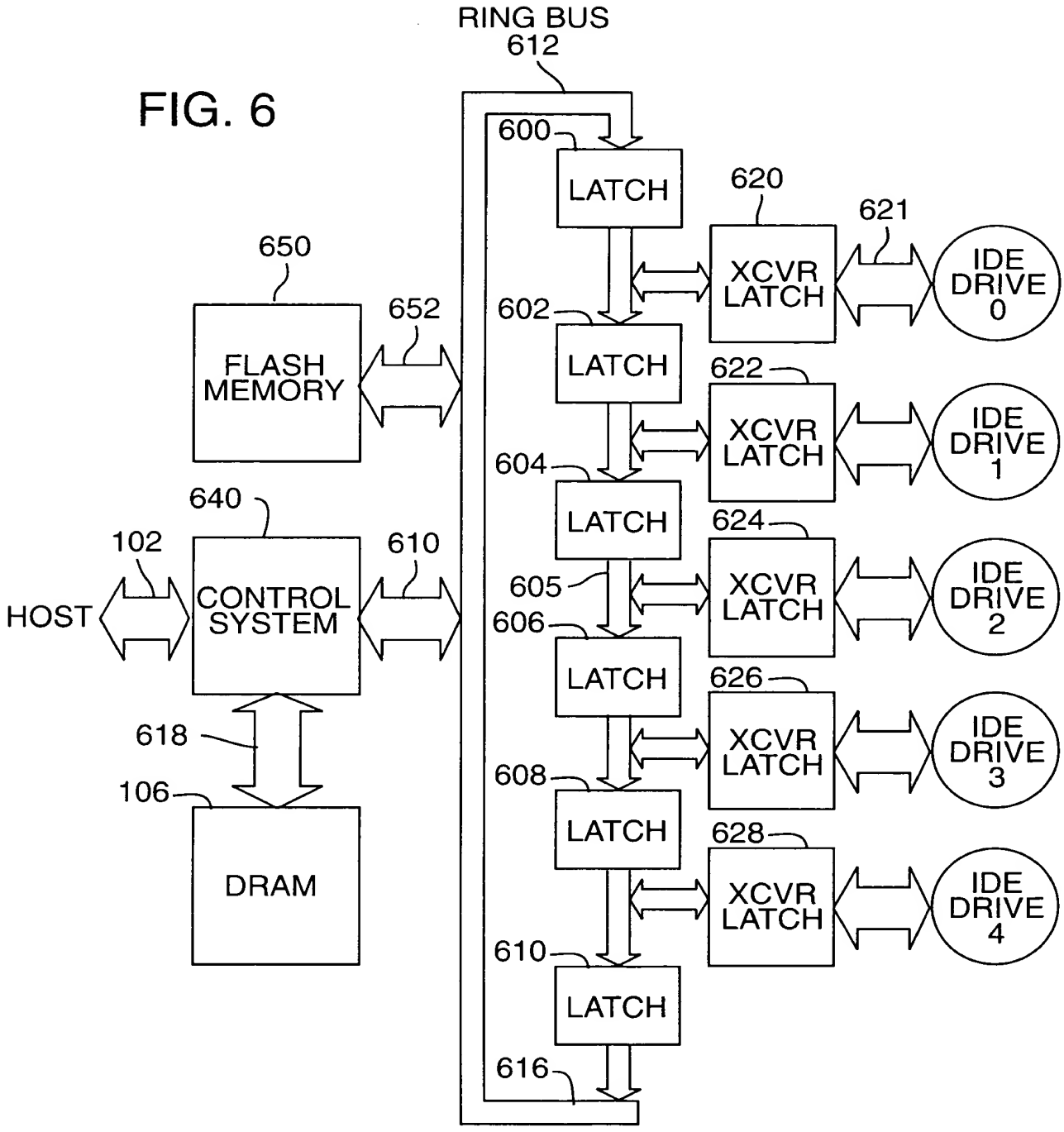


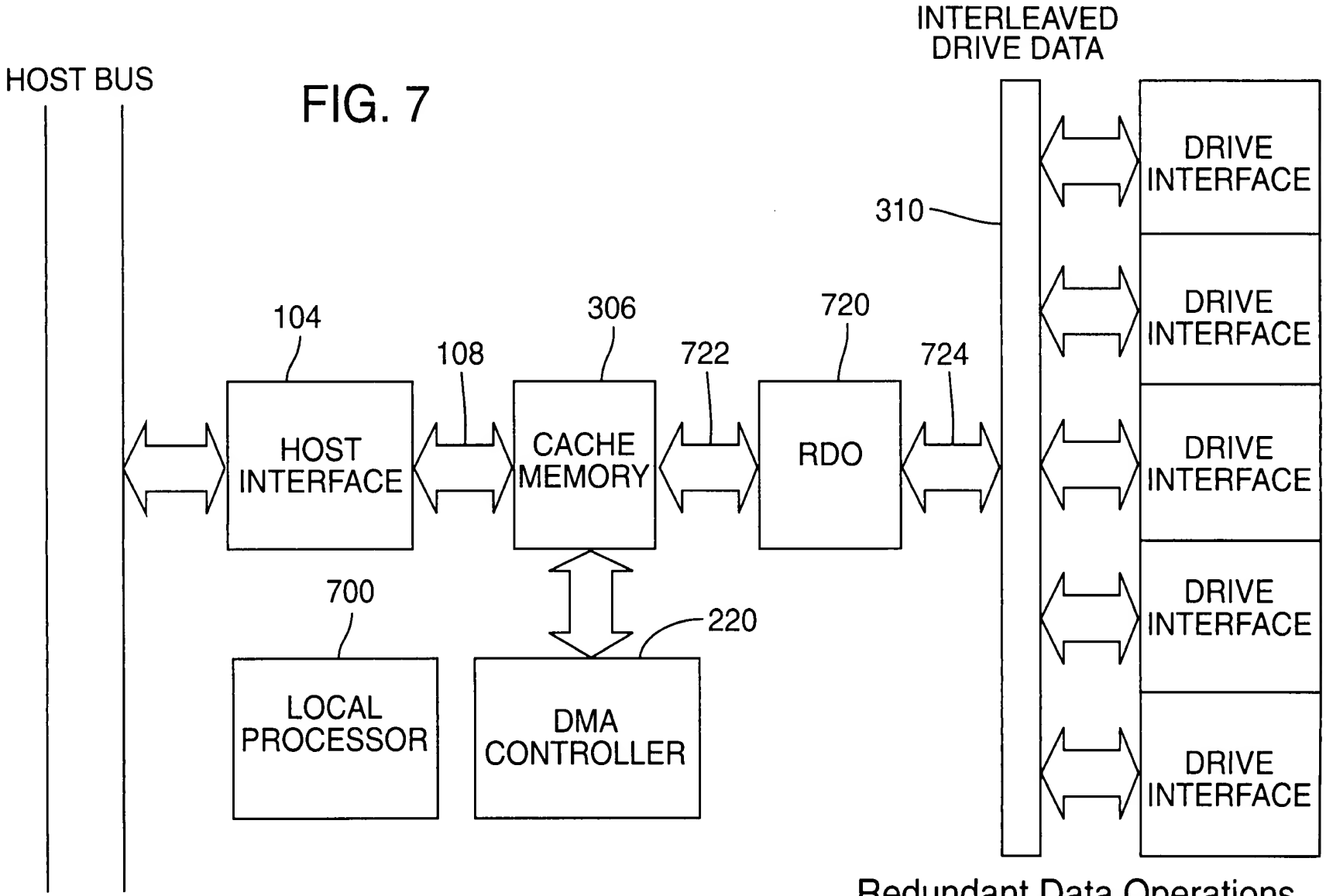
FIG. 5

FIG. 6



SYNCHRONOUS DATA TRANSFER

FIG. 7



Redundant Data Operations

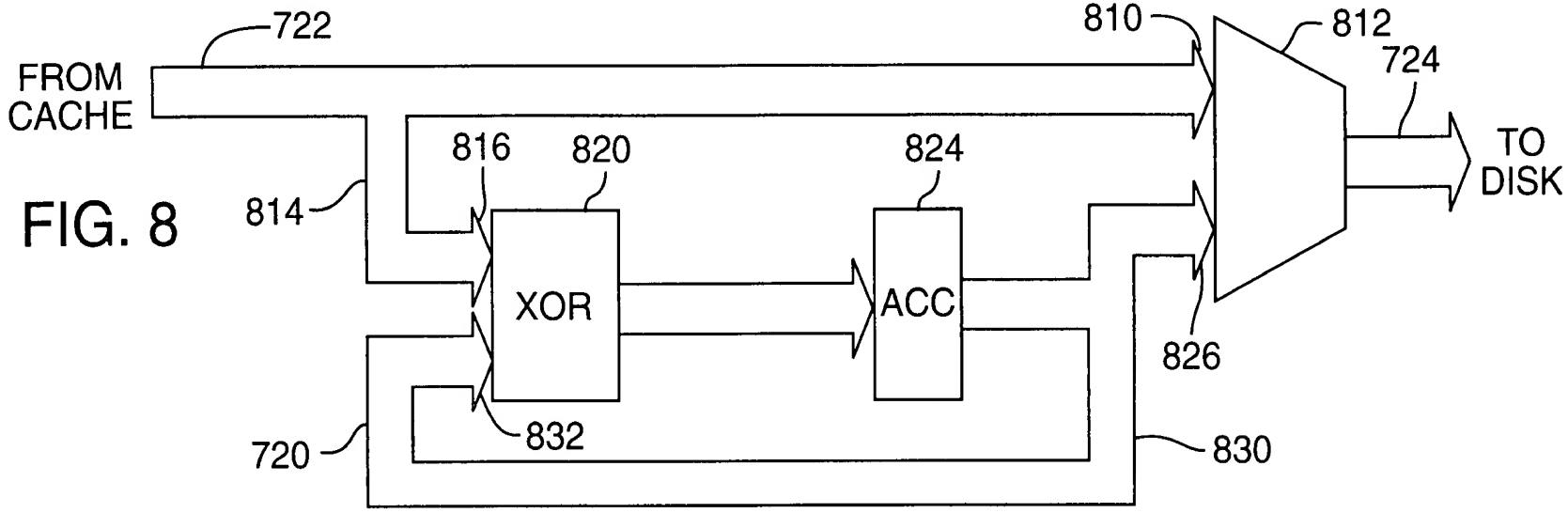


FIG. 8

FIG. 9

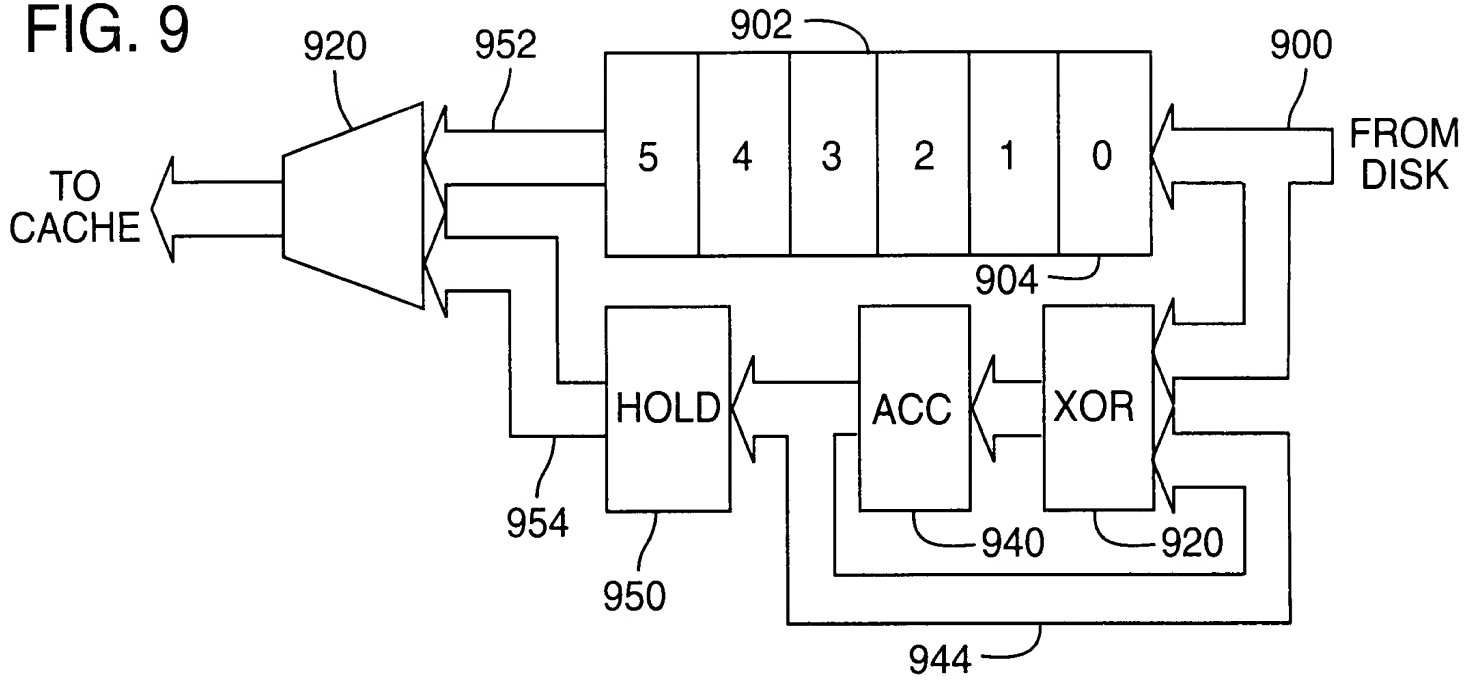


FIG. 10

DISK READ - NO CORRECTION

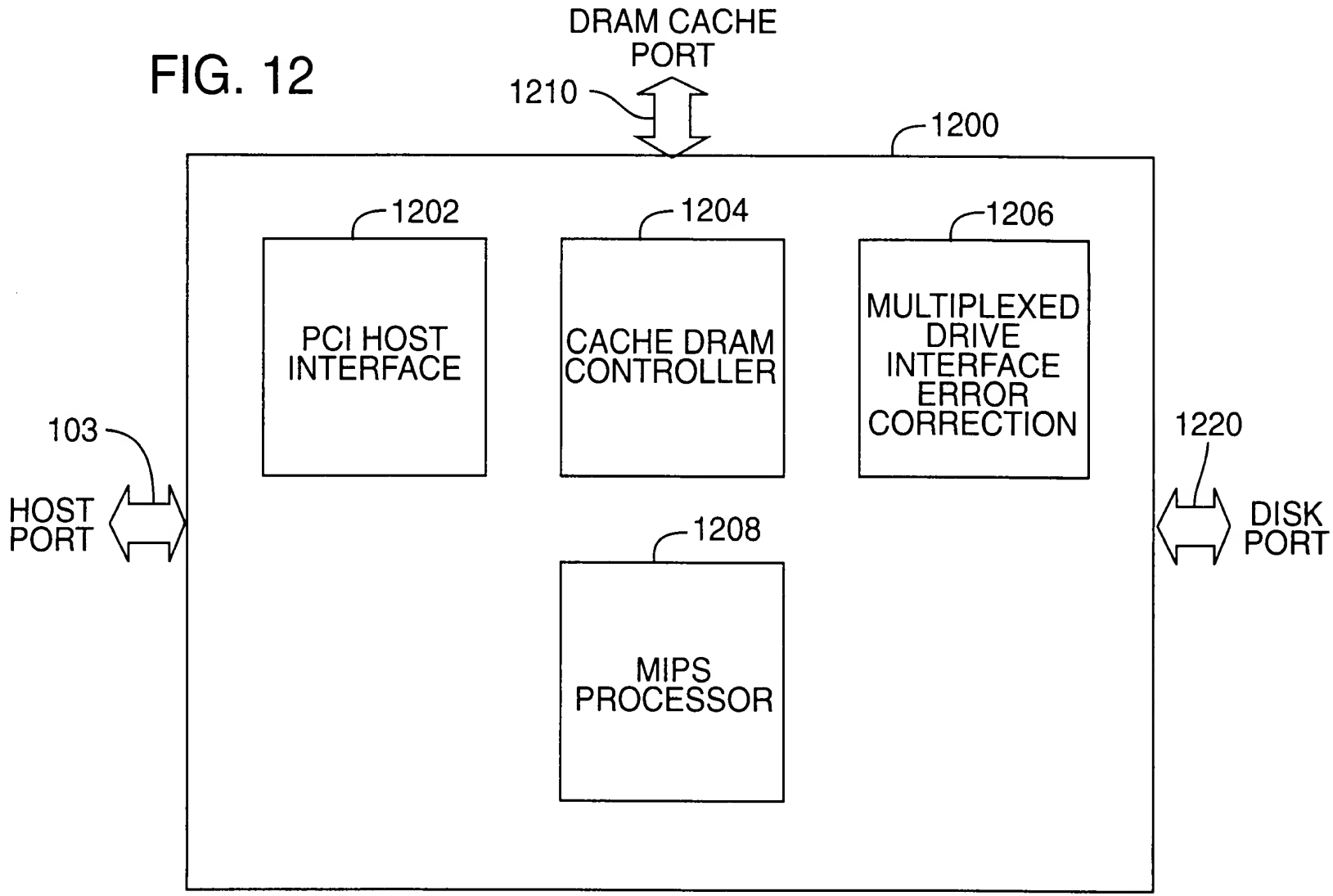
		ACCUMULATOR(940)		HOLD							
State	INPUT	FUNCT	CONTENTS	LATCH	PIPELINE(902)						OUTPUT
0	A0	LOAD	A0		A0						
1	B0	XOR	A0+B0		B0	A0					
2	C0	XOR	A0+B0+C0		C0	B0	A0				
3	D0	XOR	A0+B0+C0+D0		D0	C0	B0	A0			
4	E0	XOR	A0+B0+C0+D0+E0		E0	D0	C0	B0	A0		
0	A1	LOAD	A1	A0+B0+C0+D0+E0	A1	E0	D0	C0	B0	A0	
1	B1	XOR	A1+B1	A0+B0+C0+D0+E0	B1	A1	E0	D0	C0	B0	A0
2	C1	XOR	A1+B1+C1	A0+B0+C0+D0+E0	C1	B1	A1	E0	D0	C0	**B0**
3	D1	XOR	A1+B1+C1+D1	A0+B0+C0+D0+E0	D1	C1	B1	A1	E0	D0	C0
4	E1	XOR	A1+B1+C1+D1+E1	A0+B0+C0+D0+E0	E1	D1	C1	B1	A1	E0	D0
0				A1+B1+C1+D1+E1		E1	D1	C1	B1	A1	
1				A1+B1+C1+D1+E1			E1	D1	C1	B1	A1
2				A1+B1+C1+D1+E1				E1	D1	C1	**B1**
3				A1+B1+C1+D1+E1					E1	D1	C1
4				A1+B1+C1+D1+E1						E1	D1

FIG. 11

DISK READ - WITH CORRECTION

		ACCUMULATOR(940)		HOLD							
State	INPUT	FUNCT	CONTENTS	LATCH	PIPELINE(902)						OUTPUT
0	A0	LOAD	A0		A0						
1	(xxx)	HOLD	A0		(xxx)	A0					
2	C0	XOR	A0+C0		C0	(xxx)	A0				
3	D0	XOR	A0+C0+D0		D0	C0	(xxx)	A0			
4	E0	XOR	A0+C0+D0+E0		E0	D0	C0	(xxx)	A0		
0	A1	LOAD	A1	A0+C0+D0+E0	A1	E0	D0	C0	(xxx)	A0	
1	(xxx)	HOLD	A1	A0+C0+D0+E0	(xxx)	A1	E0	D0	C0	(xxx)	A0
2	C1	XOR	A1+C1	A0+C0+D0+E0	C1	(xxx)	A1	E0	D0	C0	**B0**
3	D1	XOR	A1+C1+D1	A0+C0+D0+E0	D1	C1	(xxx)	A1	E0	D0	C0
4	E1	XOR	A1+C1+D1+E1	A0+C0+D0+E0	E1	D1	C1	(xxx)	A1	E0	D0
0				A1+C1+D1+E1		E1	D1	C1	(xxx)	A1	
1				A1+C1+D1+E1			E1	D1	C1	(xxx)	A1
2				A1+C1+D1+E1				E1	D1	C1	**B1**
3				A1+C1+D1+E1					E1	D1	C1
4				A1+C1+D1+E1						E1	D1

FIG. 12



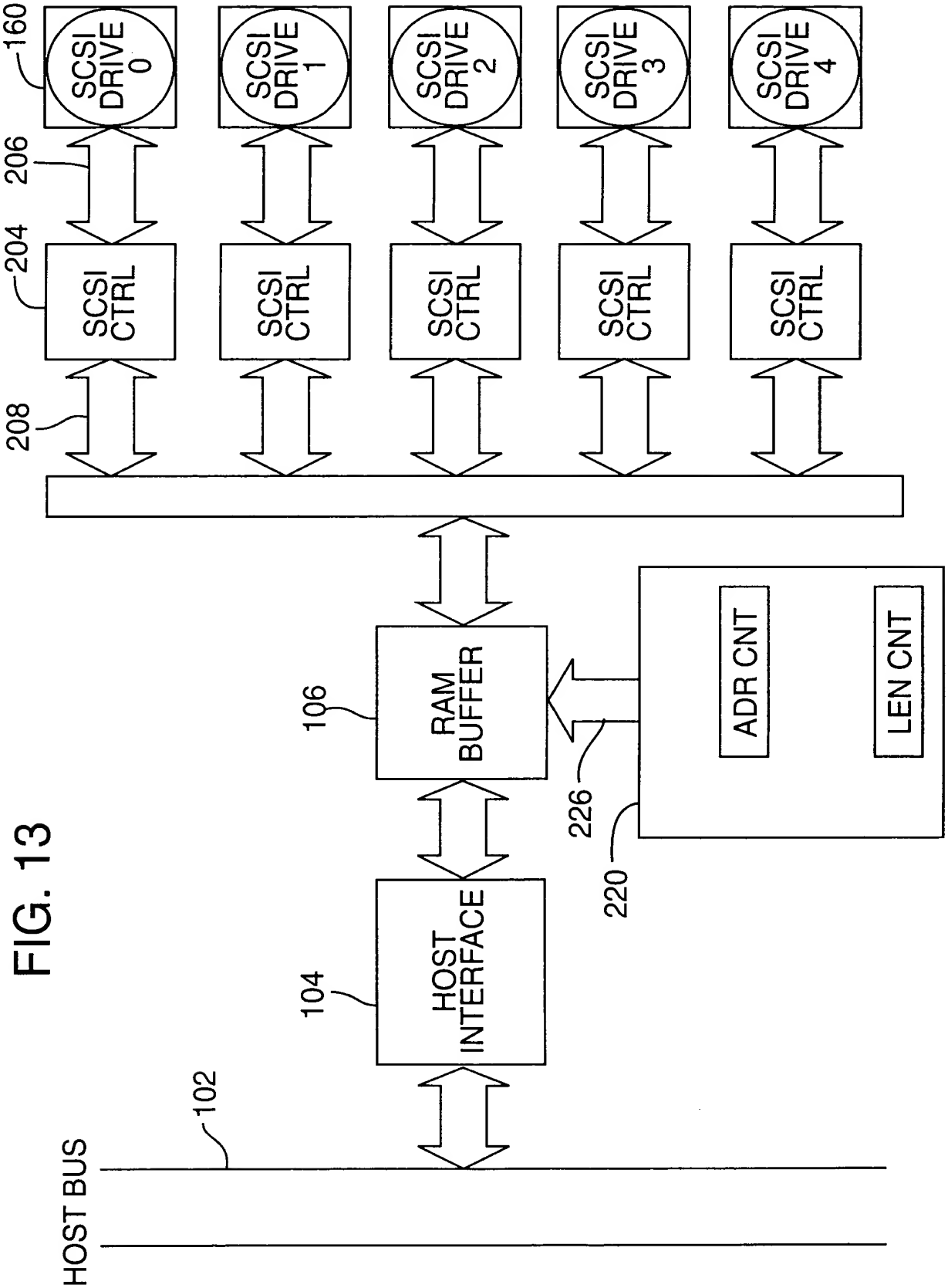


FIG. 13

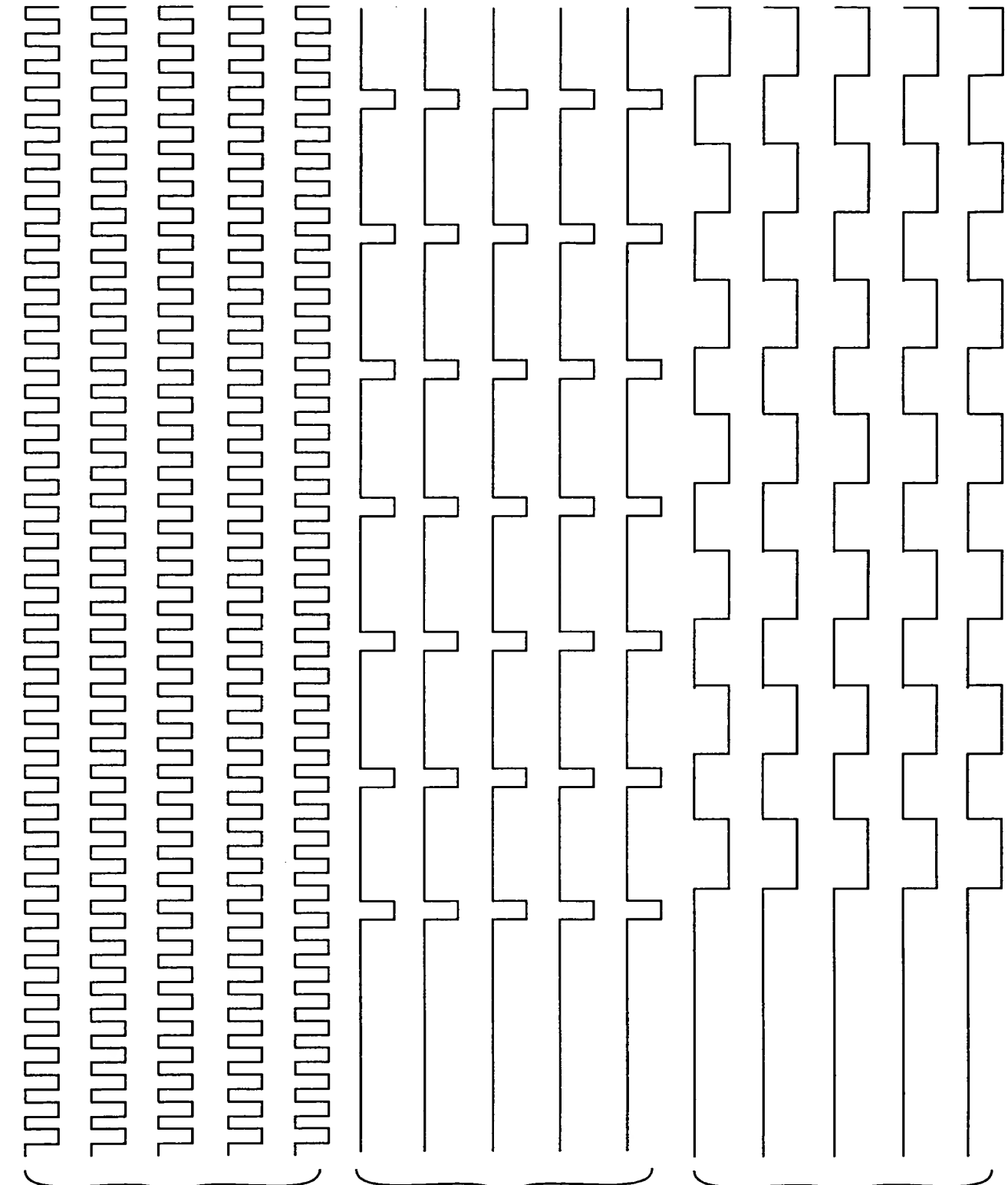


FIG. 14